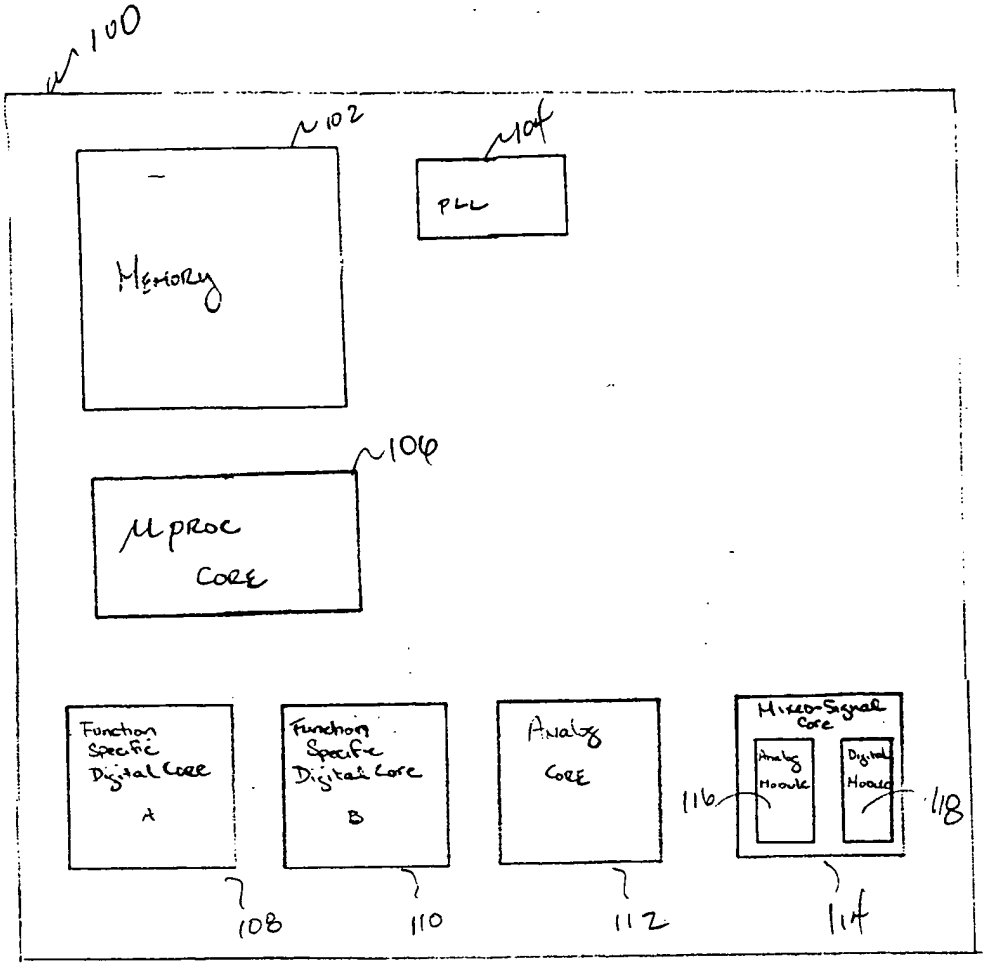
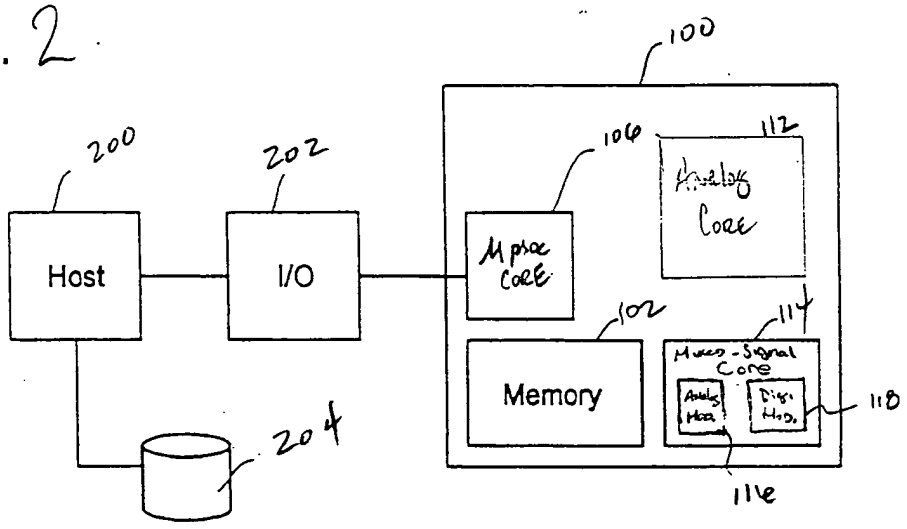

100
102
Memory
104
PLL
106
μProc
Core
Function Specific Digital Core
A
Function Specific Digital Core
B
Analog
Core
Mixed-Signal Core
Analog Module
Digital Module
116
118
108
110
112
114

Fig 1

Fig. 2


100
200
202
106
112
Host
I/O
Mproc Core
Analog Core
102
114
Memory
Mixed-Signal Core
118
116
204

TI-36267

3

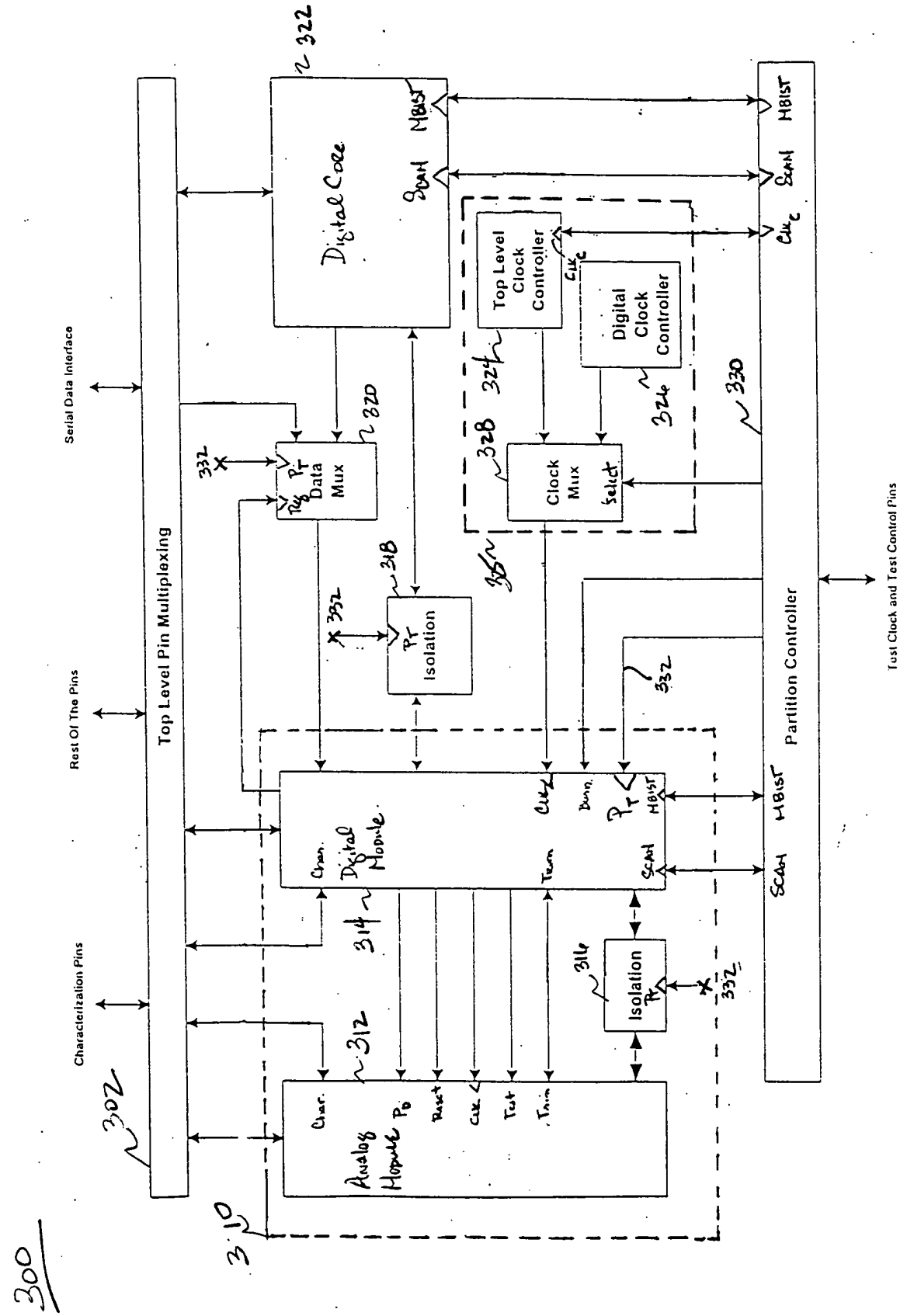


Fig 3

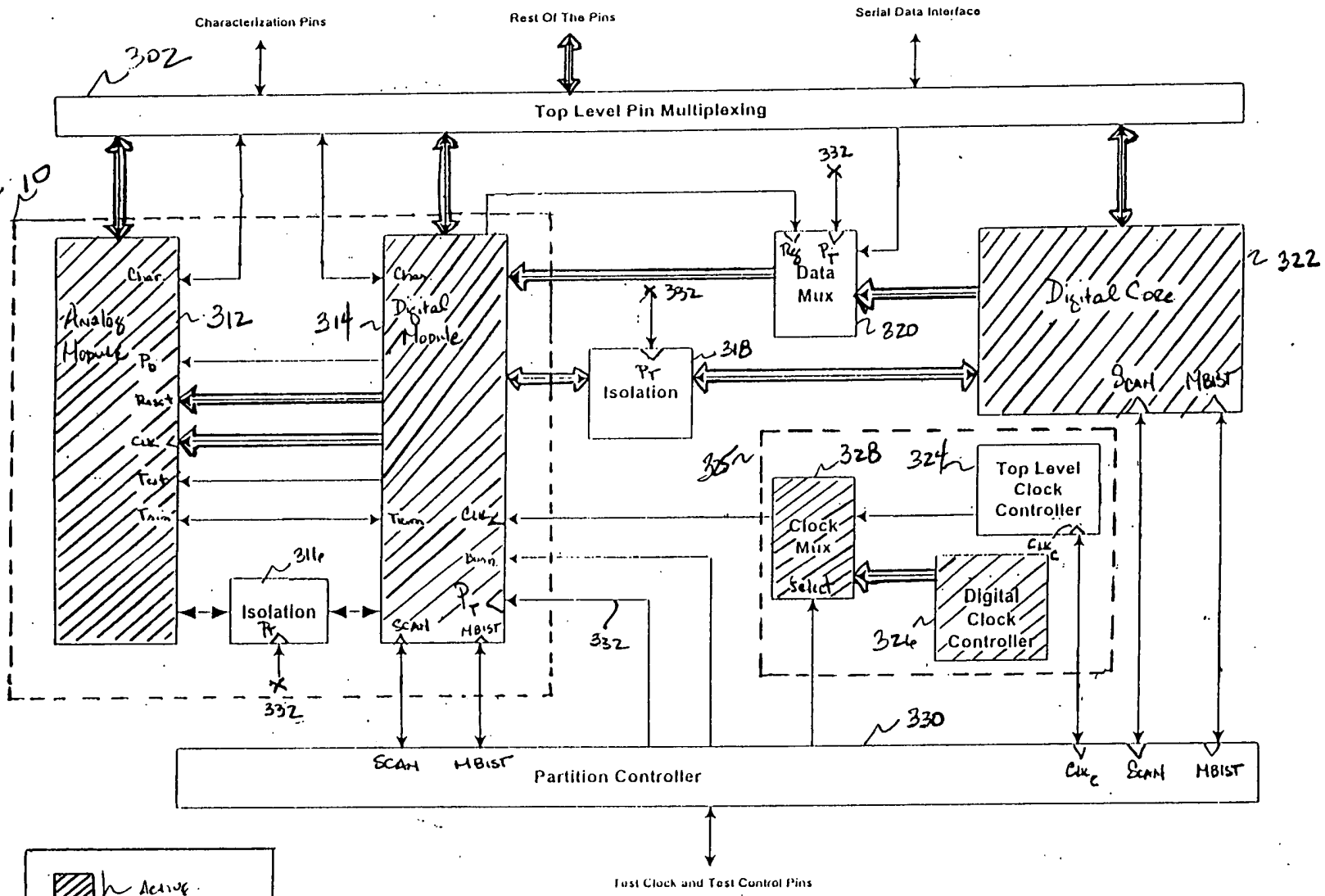


Fig 4

TI-37297
4

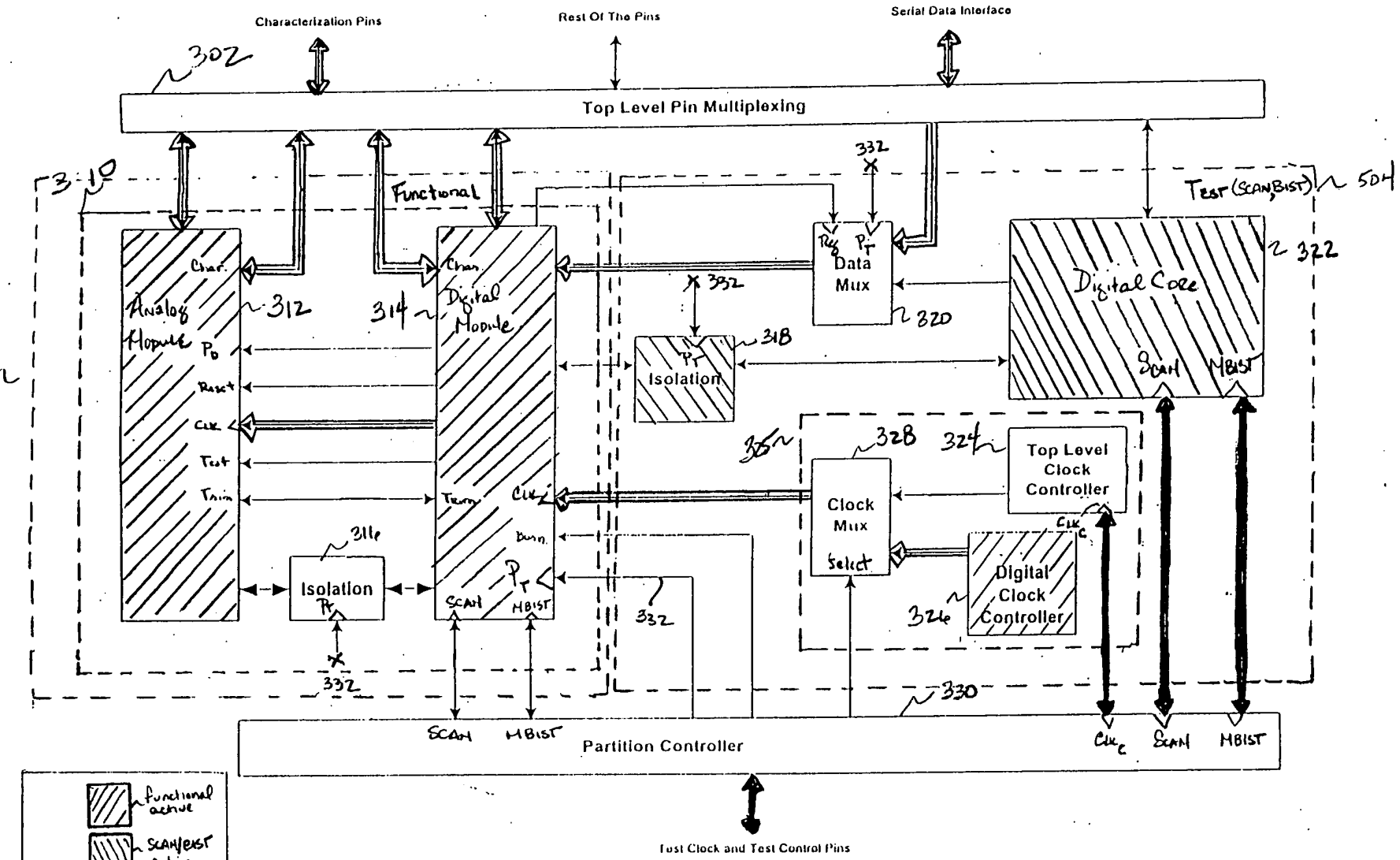


Fig 5

TI-32917
5

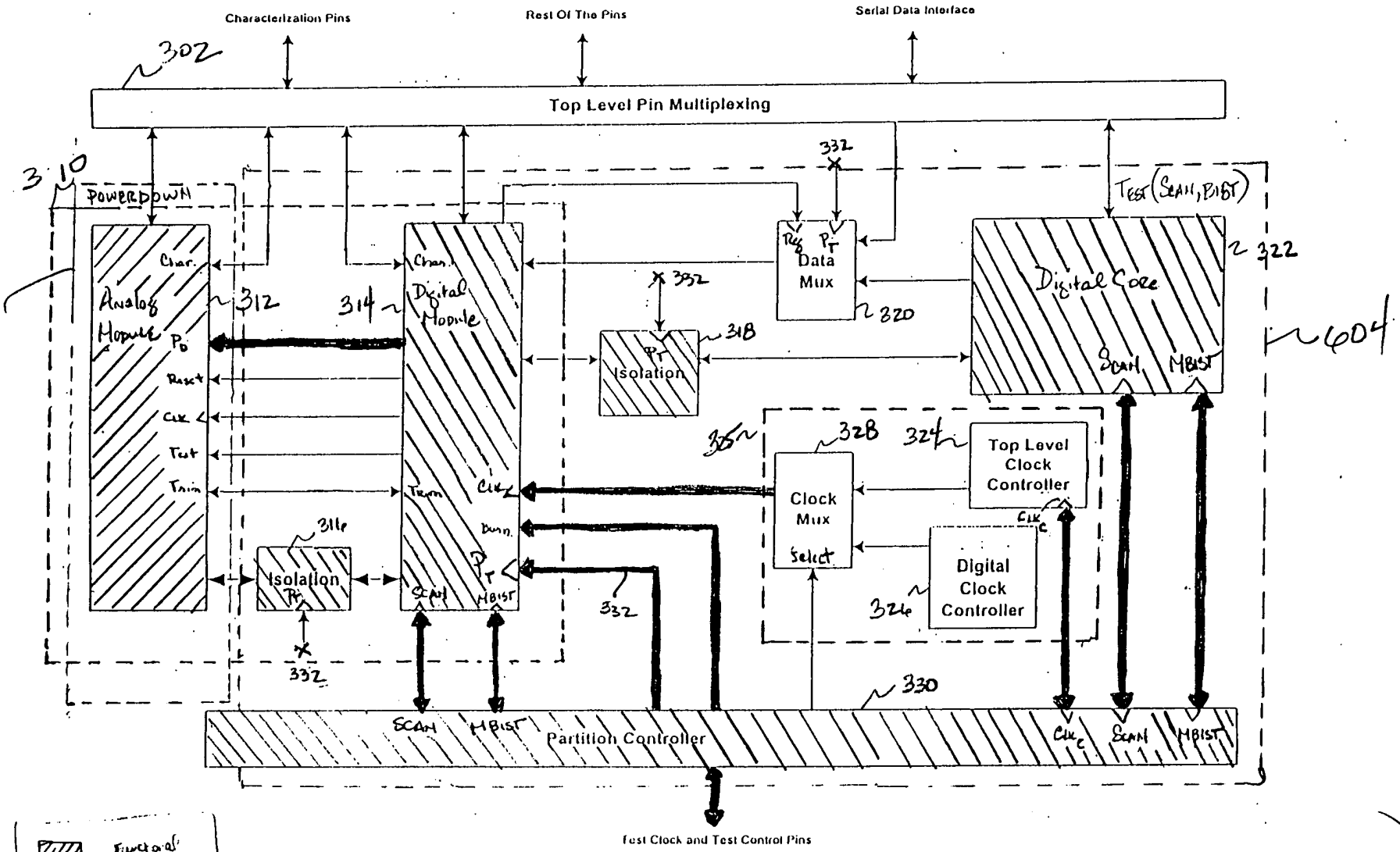


Fig 6.

TI-36967

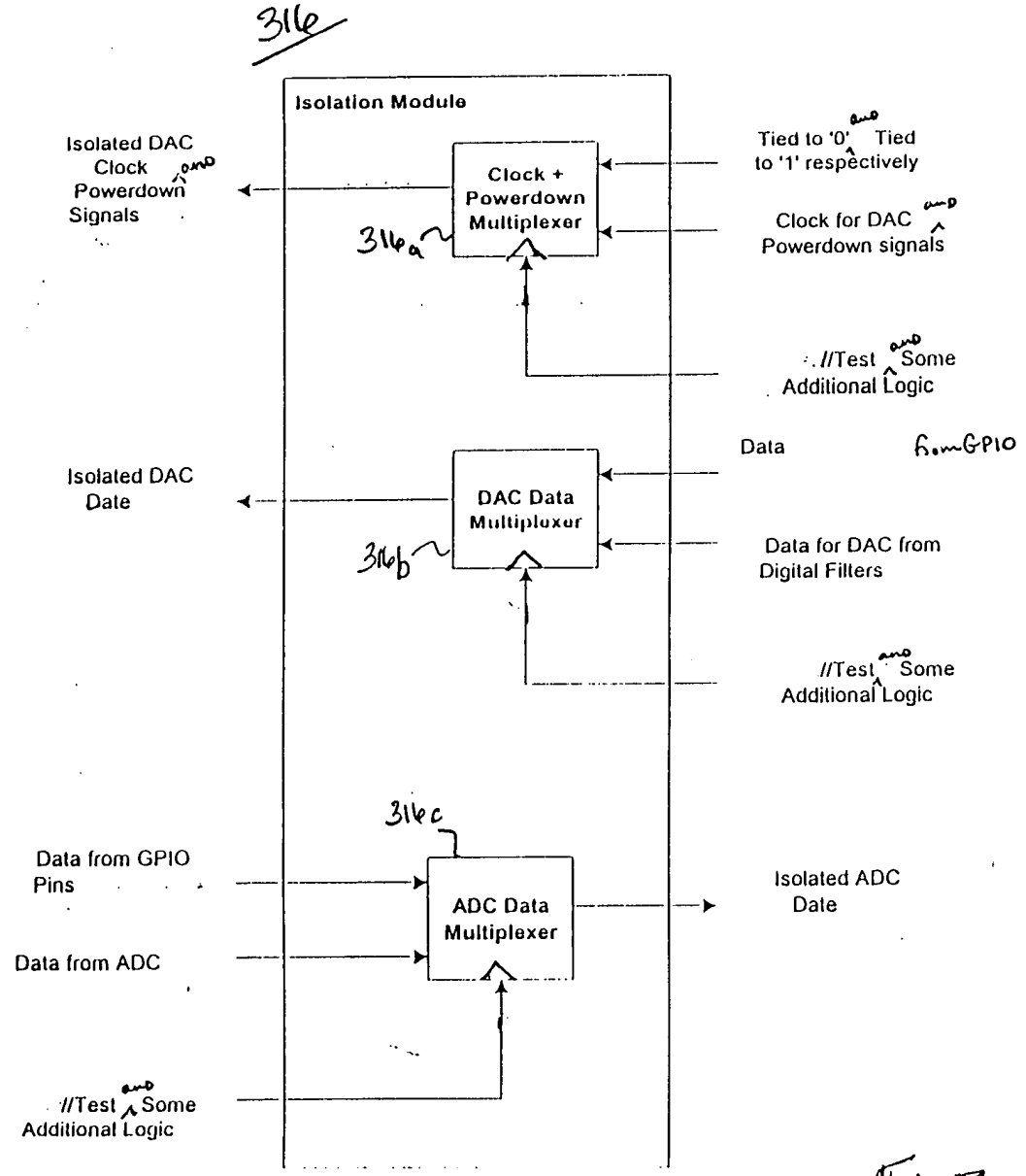

316
Isolation Module
Isolated DAC Clock and Powerdown Signals
Clock + Powerdown Multiplexer
316a
Tied to '0' and Tied to '1' respectively
Clock for DAC and Powerdown signals
//Test and Some Additional Logic
Data from GPIO
Isolated DAC Date
DAC Data Multiplexer
316b
Data for DAC from Digital Filters
//Test and Some Additional Logic
316c
Data from GPIO Pins
Data from ADC
ADC Data Multiplexer
Isolated ADC Date
//Test and Some Additional Logic

Fig 7

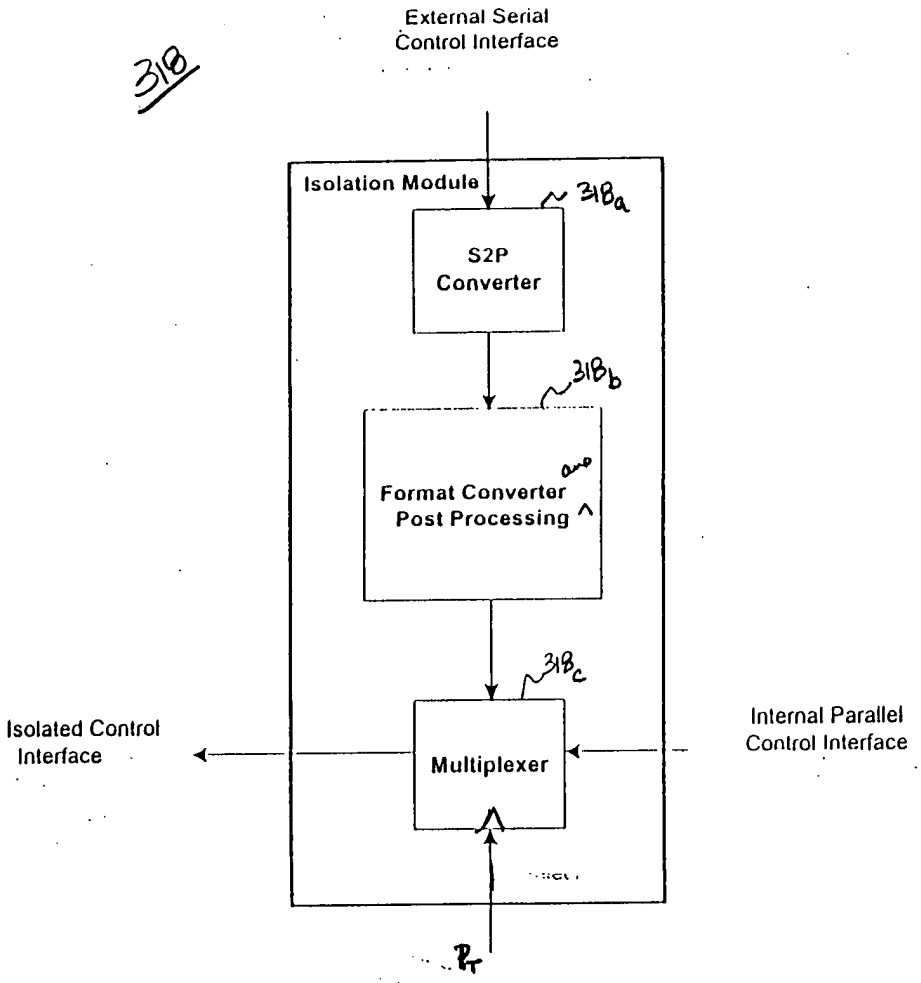

318
External Serial
Control Interface
Isolation Module
318a
S2P
Converter
318b
Format Converter
Post Processing
318c
Multiplexer
Isolated Control
Interface
Internal Parallel
Control Interface

Fig 8